

IN THE CLAIMS

This listing of claims replaces all prior versions and listings of claims in the application:

1. (Currently Amended) A light-emitting device comprising:
a ~~chip~~ semiconductor light emitting diode capable of emitting light of a first wavelength, the semiconductor light emitting diode having a light-emitting surface, and
a phosphor layer provided on a first portion of the light-emitting surface, wherein the phosphor layer is capable of converting light of the first wavelength to visible light of a second wavelength,
wherein a second portion of the light-emitting surface is without the phosphor layer, and wherein the second portion is substantially surrounded by the first portion.
2. (Previously Amended) A device as claimed in claim 1, wherein the sizes of the first portion and the second portion are such that mixing the emitted light of the first and the second wavelength results in substantially white light.
3. (Previously Amended) A device as claimed in claim 1, wherein the thickness of the phosphor layer is such that all the light of the first wavelength incident on the phosphor layer is converted to light of the second wavelength.
4. (Previously Amended) A device as claimed in claim 1, wherein the second portion without the phosphor layer is distributed over a plurality of regions on the light emitting surface.
5. (Previously Amended) A device as claimed in claim 4, wherein the plurality of regions form a pattern.
6. (Previously Amended) A device as claimed in claim 1, wherein the second portion without the phosphor layer is at least partly covered with a light-transmitting layer which is capable of spreading light incident on said second portion.

7. (Canceled).

8. (Currently Amended) A lighting device as claimed in claim 7 1, wherein the lighting device further comprises optical elements for mixing the emitted light of the first and the second wavelength.

9. (Currently Amended) A method of manufacturing a light-emitting device, the method comprising:

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at least partly surrounding providing a chip semiconductor light emitting diode capable of emitting light of a first wavelength with, the semiconductor light emitting diode having a light-emitting surface, and

providing a phosphor layer on the light-emitting surface, which phosphor layer is capable of converting light of the first wavelength to visible light of a second wavelength,

wherein the phosphor layer is removed from, or not provided on, a portion of the light-emitting surface substantially surrounded by the phosphor layer.

10. (Previously Amended) A method as claimed in claim 9, wherein the phosphor layer is provided on the light-emitting surface by means of screen printing.

Please add the following new claims:

11. (Previously Added) A device as claimed in claim 1, wherein the second portion is completely surrounded by the first portion.

12. (Previously Added) A device as claimed in claim 1, wherein the second portion is disposed in a path of light emitted by the chip.

13. (Previously Added) A device as claimed in claim 1, wherein the light-emitting surface is disposed in a path of light emitted by the chip.

14. (Currently Added) A light-emitting device comprising:

a ~~chip~~ semiconductor light emitting diode capable of emitting light of a first wavelength, the semiconductor light emitting diode having a light-emitting surface, and

a plurality of regions of phosphor provided on the light-emitting surface, wherein the plurality of regions of phosphor are capable of converting light of the first wavelength to visible light of a second wavelength.

15. (Previously Added) A device as claimed in claim 14, wherein the plurality of regions of phosphor are separated by regions of the light-emitting surface without phosphor.

16. (Previously Added) A device as claimed in claim 15, wherein the regions of the light-emitting surface without phosphor are at least partly covered with a light-transmitting layer.

17. (Previously Added) A device as claimed in claim 16, wherein a thickness of the light-transmitting layer is substantially the same as a thickness of phosphor in the regions of phosphor.

18. (Previously Added) A device as claimed in claim 14, wherein the plurality of regions of phosphor form a pattern.

19. (Previously Added) A device as claimed in claim 18, wherein the pattern is a chessboard pattern.